

Video Buffer Detail

The purpose of the video buffer is to provide a method for scaling the raw video signal so that it is optimized for a frame grabber, and also to provide a TTL level frequency output for slow scan image acquisitions.

In order to make the video buffer as versatile as possible, the following specifications are desired:

Input voltage ranges:

-10 VDC to +10VDC, scaleable by resistor selection and potentiometer to any values within the maximum range. For instance, with the proper gain and offset resistors the input range could be zero to + 3VDC. Or 0 to -6V DC.

Output ranges:

-5VDC to +5vDC, scaleable by resistor selection and potentiometer adjustment to any values within the range. For instance, 0 to -3 V DC (as for the DT3152 board).

Frequency conversion:

A V/F function will provide 0 to 1MHZ TTL output for whatever output range is selected. For instance, if the range of output of 0 to -3VDC, then 0 VDC = 0 Hz, and -3V DC = 1mhz.

